

Description

The LY02AC03L is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. It complies with IEC 61000-4-2 (ESD), $\pm 30\text{kV}$ air and $\pm 30\text{kV}$ contact discharge. It is assembled into an ultra-small lead-free DFN0603-2 package. The small size and high ESD surge protection make it an ideal choice to protect cell phone, digital cameras and other portable applications.

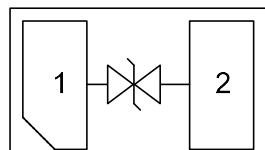
Features

- Low clamping voltage
- Ultra low leakage current
- Operating voltage: 3.3V
- RoHS compliant
- IEC-61000-4-2 ESD $\pm 30\text{kV}$ Air, $\pm 30\text{kV}$ Contact
- Packaging: 7 inch reel, 10000pcs/reel

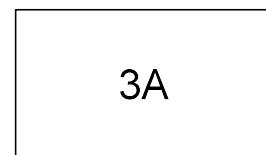
Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation
- Digital Cameras
- Audio Players

Pin Configuration and Marking



Circuit and Pin Schematic



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

Parameter	Symbol	Value
Peak Pulse Power (8/20μs)	P_{PP}	80W
Peak Pulse Current (8/20μs)	I_{PP}	8A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	±30kV ±30kV
Ambient Temperature Range	T_A	-55°C to +125°C
Storage Temperature Range	T_{STG}	-55°C to +150°C

Electrical Characteristics ($T_A=25^\circ\text{C}$)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.
Reverse Working Voltage	V_{RWM}		-	-	3.3V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	3.8V	-	-
Reverse Leakage Current	I_R	$V_{RWM} = 3.3\text{V}$	-	-	0.2μA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}$ (8/20μs)	-	-	6V
		$I_{PP} = 8\text{A}$ (8/20μs)	-	-	10V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$	-	-	25pF

Typical Characteristic Curves ($T_A=25^\circ\text{C}$)

Figure 1. Peak Pulse Power Rating Curve

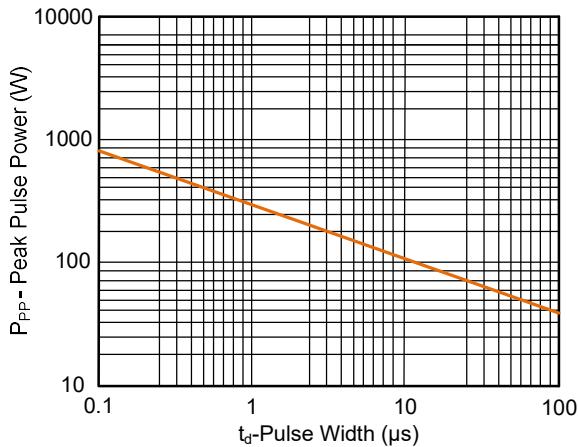


Figure 2. Pulse Derating Curve

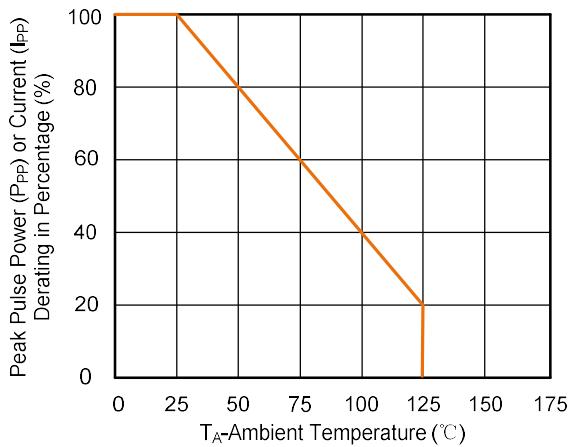


Figure 3. Clamping Voltage vs. Peak Pulse Current

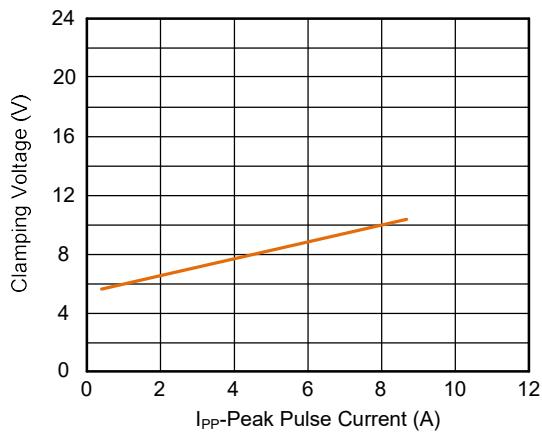


Figure 4. Junction Capacitance vs. Reverse Voltage

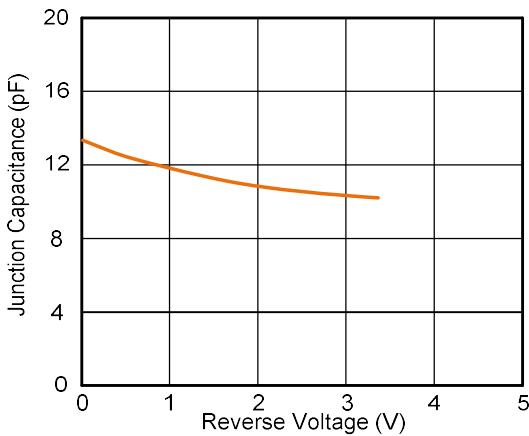


Figure 5. Pulse Waveform (8/20μs)

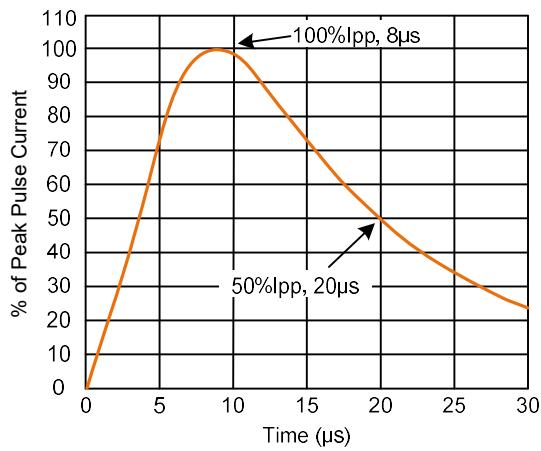
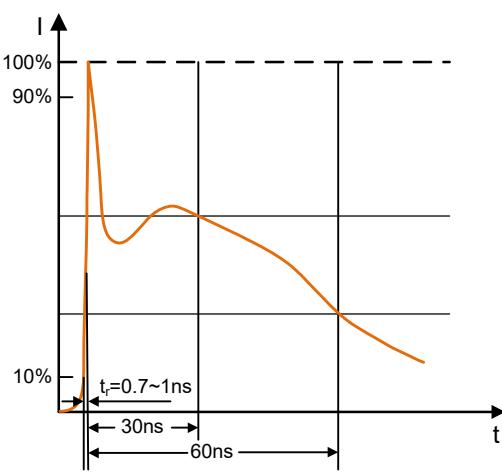
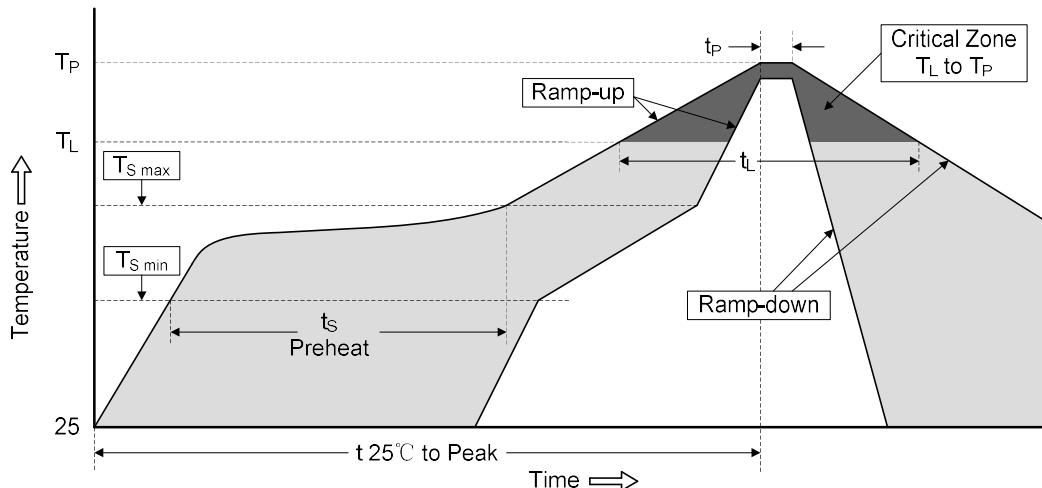


Figure 6. Pulse Waveform (IEC61000-4-2)



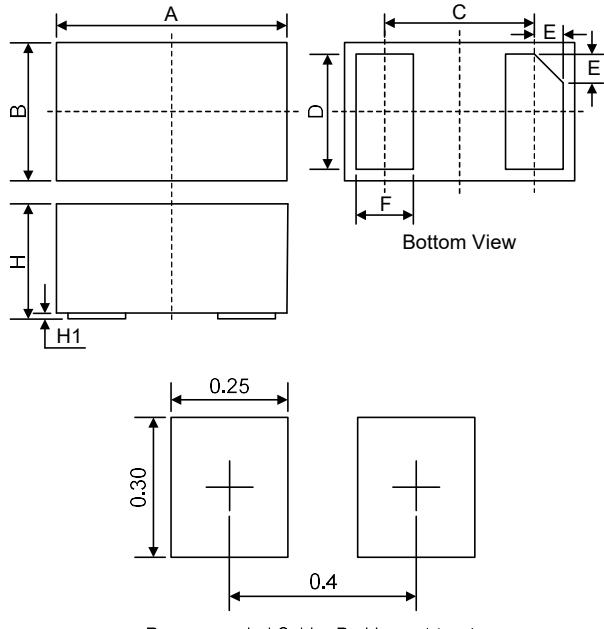
Soldering Parameters

Reflow Soldering



Profile Feature	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.
Preheat	
-Temperature Min ($T_S \text{ min}$)	150°C
-Temperature Max ($T_S \text{ max}$)	200°C
-Time (min to max) (t_s)	60-180 seconds
$T_S \text{ max}$ to T_L	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature (T_L)	217°C
-Time (t_L)	60-150 seconds
Peak Temperature (T_P)	260°C
Time within 5°C of actual Peak Temperature (t_P)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Dimensions (DFN0603-2)



Top View

Bottom View

Recommended Solder Pad Layout (mm)

Symbol	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.55	0.65	0.022	0.026
B	0.25	0.35	0.010	0.014
C	0.36BSC		0.014BSC	
D	0.22	0.28	0.009	0.011
E	0.08BSC		0.003BSC	
F	0.16	0.22	0.006	0.009
H	0.23	0.33	0.009	0.013
H1	0.00	0.05	0.000	0.002